

Dual Drive

JFET Overdrive / Opamp Distortion

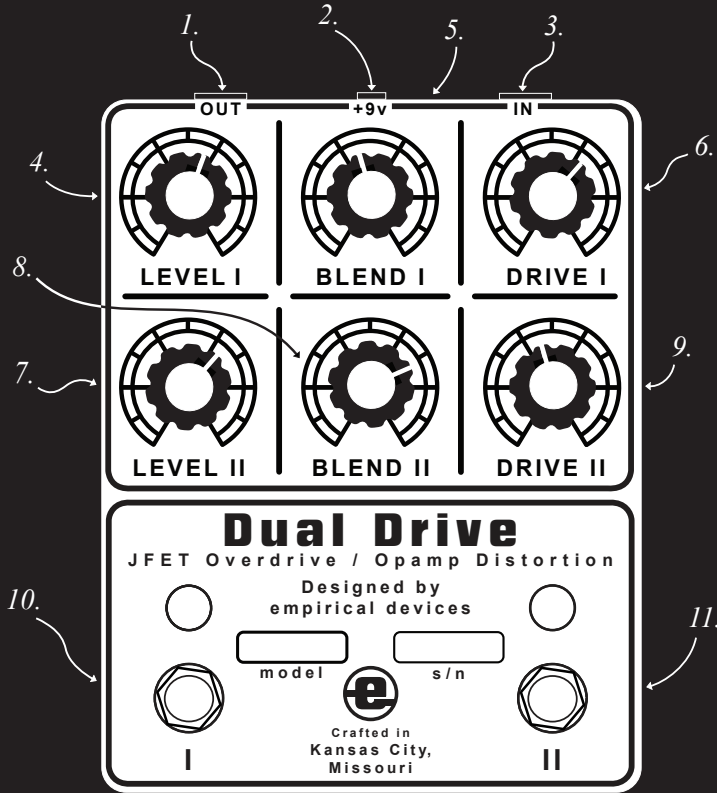


Fig. 1

- [1.] **OUT** sends audio signal to the amplifier.
- [2.] **+9V** accepts DC power from 9 volt power supply.
- [3.] **IN** receives audio signal from the instrument.
- [4.] **LEVEL I** adjusts the overall volume level of the “Drive I” circuit.
- [5.] **BLEND I** regulates the amount of audio signal affected by the “Drive I” circuit.
- [6.] **DRIVE I** controls the gain level of the “Drive I” circuit.
- [7.] **LEVEL II** adjusts the overall volume level of the “Drive II” circuit.
- [8.] **BLEND II** regulates the amount of audio signal affected by the “Drive II” circuit.
- [9.] **DRIVE II** controls the gain level of the “Drive II” circuit.
- [10.] **I** engages the “Drive I” JFET overdrive circuit.
- [11.] **II** engages the “Drive II” Opamp distortion circuit.